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In re Application of:

Shin-itsu TAKEHASHI et al.

Serial No.: 10/088,264

Group Art Unit: 2823

Filed: March 18, 2002

Examiner: William D. Coleman

For: THIN FILM TRANSISTOR AND METHOD
FOR FABRICATING THE SAMERESPONSE TO RESTRICTION REQUIREMENTCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450PLEASE ACCEPT THIS AS
AUTHORIZATION TO DEBIT
OR CREDIT FEES TO
DEP. ACCT. 16-0331
PARKHURST & WENDEL

Sir:

In response to the Office Action mailed September 29, 2003, applicants hereby provisionally elect, with traverse, to prosecute the claims of Group II (claims 24-46, 28, 29 and 32-33) in this application.

However, applicants traverse the restriction requirement since the subject matter of all of claims 1-19, 24-26, 28-29, 32-39, 44-46 and 52-54 is sufficiently related that a thorough and complete search for the subject matter of the elected claims would necessarily encompass a thorough and complete search for the

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subject matter of the non-elected claims. Search and examination of the entire application could be made without serious burden. See MPEP §803 which clearly states that "[i]f the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits." This policy should apply in the present application to avoid unnecessary delay and expense to applicants and duplicative examination by the Patent Office.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.



Roger W. Parkhurst
Registration No. 25,177

October 21, 2003

Date

RWP/mhs

Attorney Docket No.: OGOH:108

PARKHURST & WENDEL, L.L.P.
1421 Prince Street, Suite 210
Alexandria, Virginia 22314-2805
Telephone: (703) 739-0220